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REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2157

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/399,281
Filed: September 17, 1999
Inventor(s):
James B. Keller, Derrick R.
Meyer

Title: Virtual Channels and
Corresponding Buffer
Allocations for Deadlock-
Free Computer System
Operation

§ Examiner: Etienne, Ario
§ Group/Art Unit: 2157
§ Atty. Dkt. No: 5500-46300
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Lawrence J. Merkel

Printed Name

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Date

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RESPONSE TO OFFICE ACTION OF
August 8, 2002

NOV 19 2002
Technology Center 2100

ATTN: BOX AF
Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This paper is submitted in response to the Office Action of August 8, 2002, to further highlight why the application is in condition for allowance.

REMARKS

Claims 1, 3-9, and 12-29 remain pending. **Please note that the status of the claims presented in the present Office Action is incorrect.** Applicant cancelled claims 10-11 in the previous response, not claims 11-12. In the present Office Action, claims 1, 3-4, 6-10, 13, 20-22, and 29 were rejected under 35 U.S.C. § 102(e) as being anticipated by Naven, U.S. Patent No. 5,936,956 ("Naven"). Claims 5, 14-19, and 23-28 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Naven. Applicants respectfully traverse these rejections and request reconsideration.